Dimensions (left view):
- 83.70±0.15(COVER OUTLINE)
- 72.53(COVER V.A.)
- 71.86(LCD A.A.)
- 41.85±0.50
- 5.59
- 5.92
- 4.59
- 5.09
- 40.17±0.50
- (0,0)
- 4-R1.0
- 83.70±0.15(COVER OUTLINE)
- 71.18(COVER V.A.)
- 70.18(LCD A.A.)
- 3.95" TFT
- 480*(RGB)*480
- FULL VIEWING ANGLE
- SCANNING DIRECTION
- INK ON REAR SIDE OF COVER COLOR: BLACK
- PULL TAPE
- COMPONENT AREA
- 22.1±0.5
- 10.77±0.5
- 4.4
- 29.58±0.50
- 33.08
- 4.5±0.5
- 1   50

Cross-section (middle view):
- 2.83±0.3
- 0.70(COVER)
- 0.20(OCA)
- 1.93(TFT)
- 2.5MAX.
- PI STIFFENER — CONTACT SIDE
- 0.30±0.05

Right view:
- 2-3.0
- 4.44
- R3.0
- 74.83±0.2(LCM OUTLINE)
- 3.44
- 3-0.3
- 2.6
- 3M VHB 4914 T=0.25MM
- METAL FRAME (SS)
- 78.98±0.2(LCM OUTLINE)
- 4.1±0.5
- 2-1.0±0.5
- 3.8±0.3
- 50   1
- CONDUCTIVE ADHESIVE
- PULL TAPE
- 4.1±0.5
- P=0.50
- W=0.35
- 0.5±0.1
- P0.5*(50-1)=24.50±0.05
- 25.5±0.1

PIXEL DETAIL (SCALE 80:1):
- 0.1497
- 0.1462
- R G B R
- R G B R

BACKLIGHT CIRCUIT DIAGRAM:
- LEDA — LEDK

PIN ASSIGNMENT

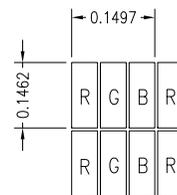| PIN	SYMBOL
1	GND
2	LEDA
3	LEDK
4	VCC
5	IOVCC
6	SDO
7	SDI
8	GND
9	SCL/WR
10	CS
11	NC
12	RESET
13	R0
14	R1
15	R2
16	R3
17	R4
18	R5
19	R6
20	R7
21	G0
22	G1
23	G2
24	G3
25	G4
26	G5
27	G6
28	G7
29	B0
30	B1
31	B2
32	B3
33	B4
34	B5
35	B6
36	B7
37	DE
38	GND
39	PCLK
40	GND
41	HS
42	VS
43	TP_GND
44	LED_PWM
45	TP_INT
46	TP_SDA
47	TP_SCL
48	TP_RESET
49	TP_VCI
50	TP_IOVCC

NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.), IOVCC=1.8V/3.3V
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: ST7701S
5. BACKLIGHT TYPE: WHITE LED, IF=40MA, VF=12.0V~13.6V
6. RECOMMEND MATCHING CONNECTOR: HRS FH28-50S-0.5SH OR EQUIVALENT
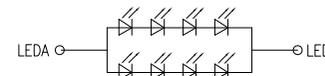
NOTES FOR CTP:

1. 4.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR (ON-CELL) + FPCA
3. OPERATING VOLTAGE: TP_VCI=2.8V~3.3V
4. DRIVER IC: CST3240
5. INTERFACE: IIC
6. RESOLUTION: 480*480
7. MULTI-FINGER: 5
8. HARDNESS OF COVER SURFACE: ≥6H
9. FIRMWARE FILE: TBD

NOTES FOR OTHERS:

1. OPERATING TEMP: -20°C ~ 70°C
2. STORAGE TEMP: -30°C ~ 80°C
3. SURFACE LUMINANCE: 290CD/M^2 (TYP.)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. UNMARKED TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

PIXEL DETAIL (SCALE 80:1)

BACKLIGHT CIRCUIT DIAGRAM

MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	
CHECKED BY:		
		APPROVED BY:

VER.	REVISED DESCRIPTION	REVISER	DATE
01	FIRST ISSUE	RITA	2025.07.17

SCALE : 1/1
UNIT: mm
VERSION NO: 01
SHEET NO: 1/1

MODULE P/N: MIO400VT-1CP-F

DESCRIPTION: TFT + CTP MODULE